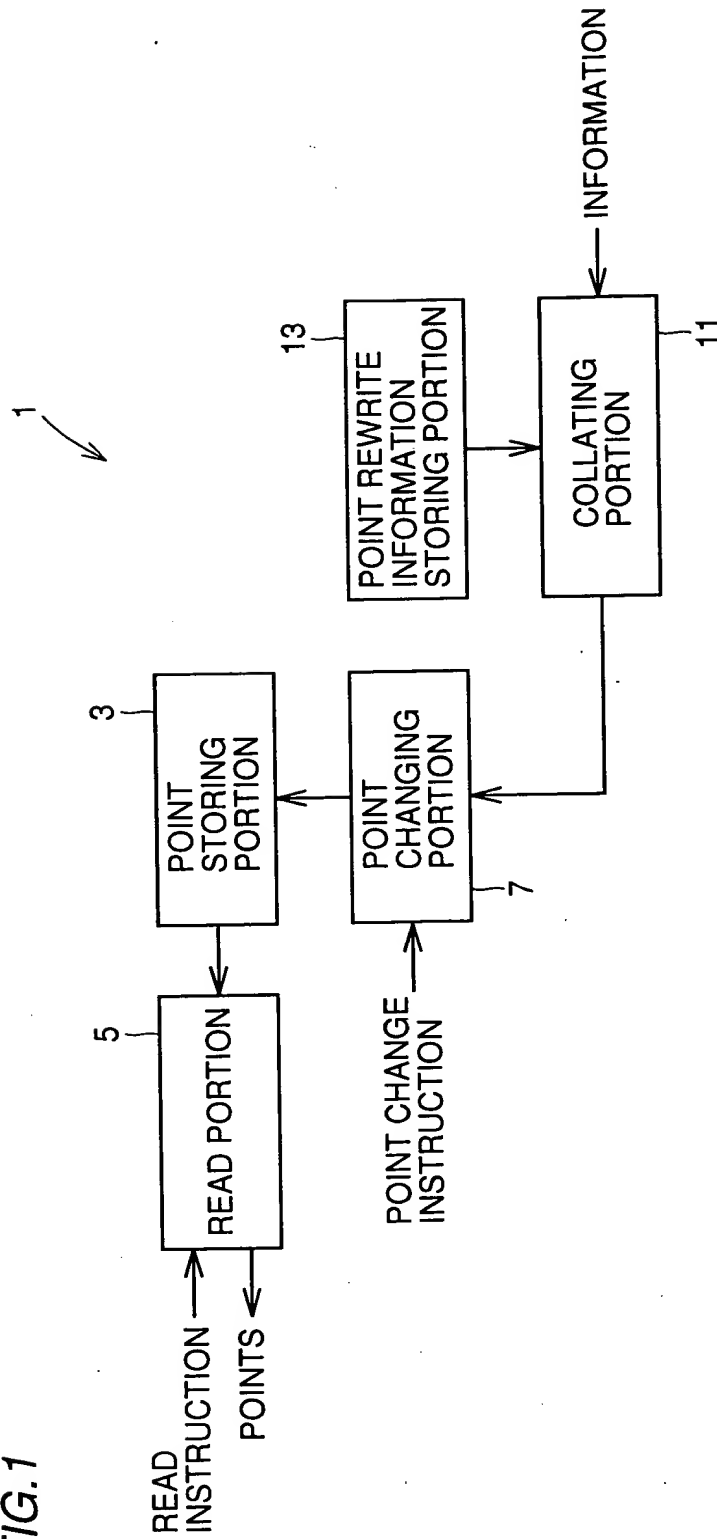
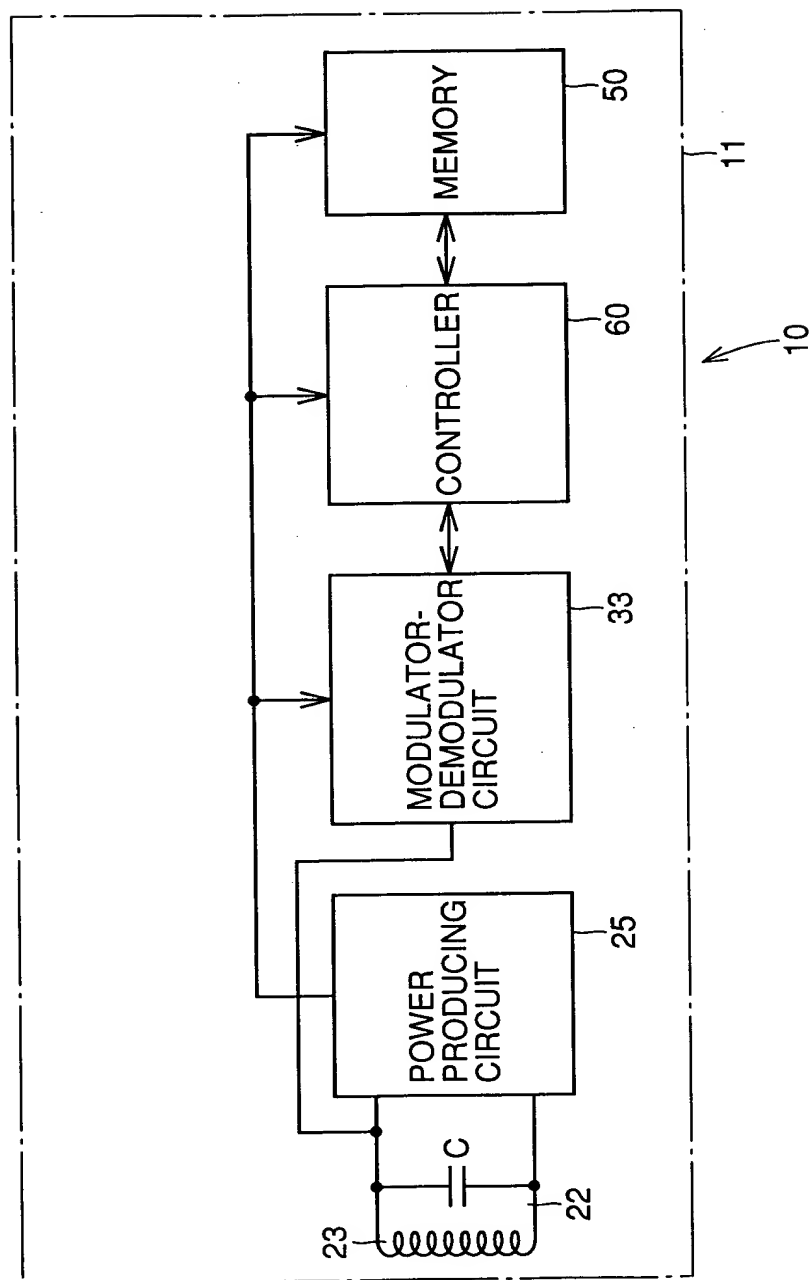


FIG.1



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FIG.2



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FIG.3

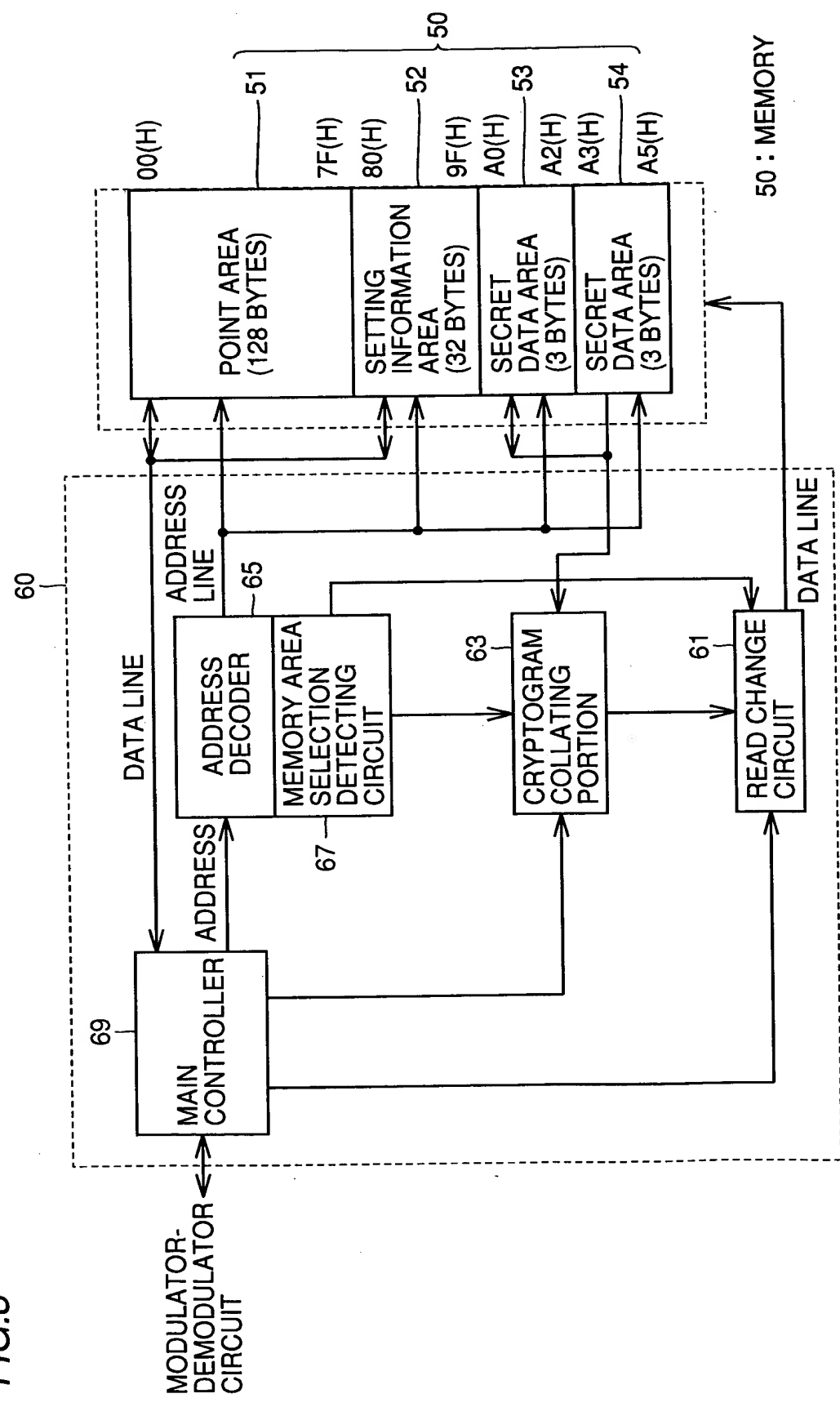


FIG.4

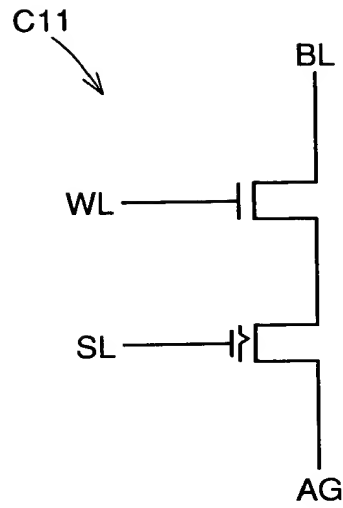


FIG.5

	BL	SL	WL	AG
READ	DATA OUTPUT	5V	5V	0V
WRITE	20V	0V	20V	OPEN
ERASE	0V	20V	20V	OPEN OR 0V

DATA CHANGE { WRITE: CHANGE "1" TO "0"
ERASE: CHANGE "0" TO "1"

FIG. 6

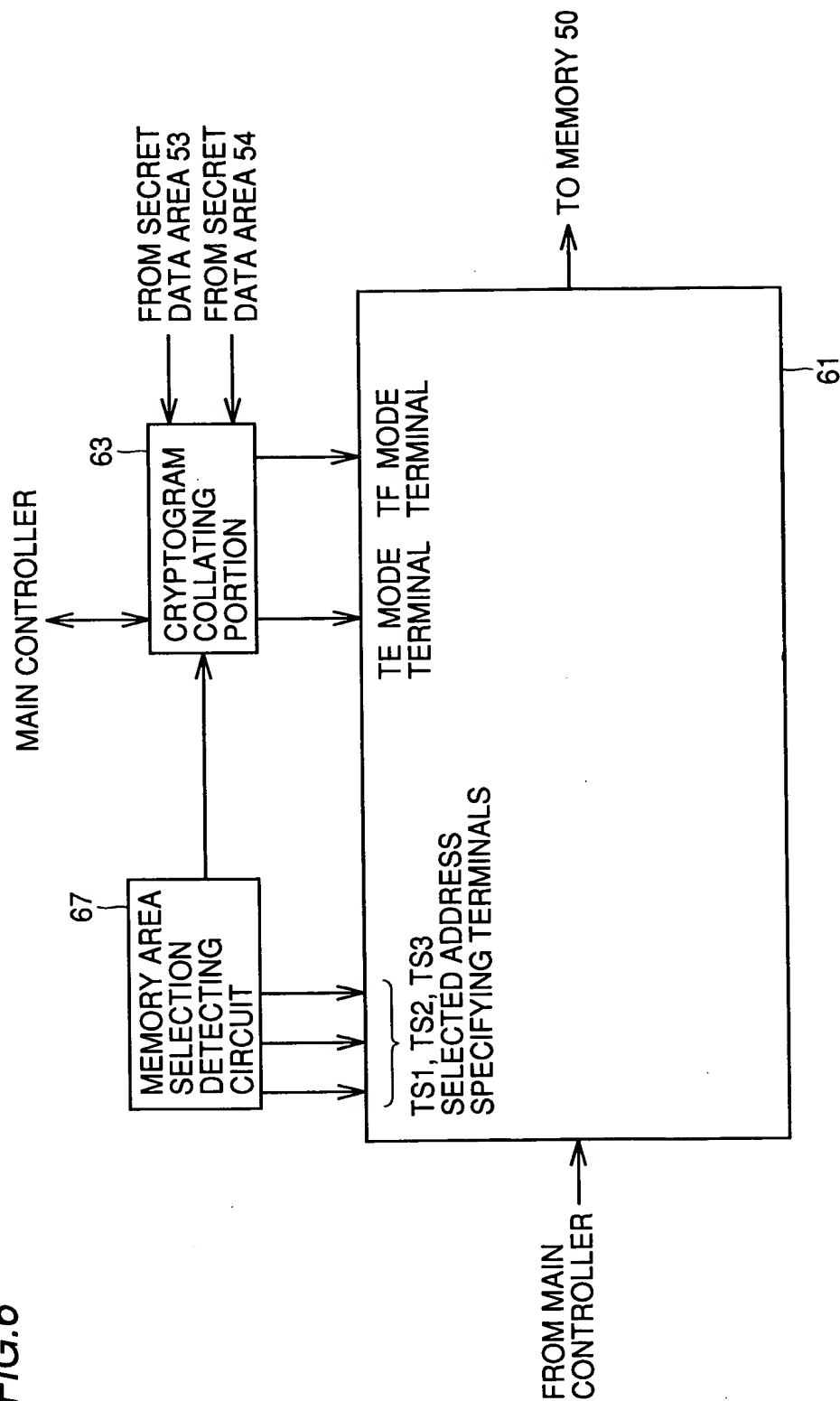


FIG. 7

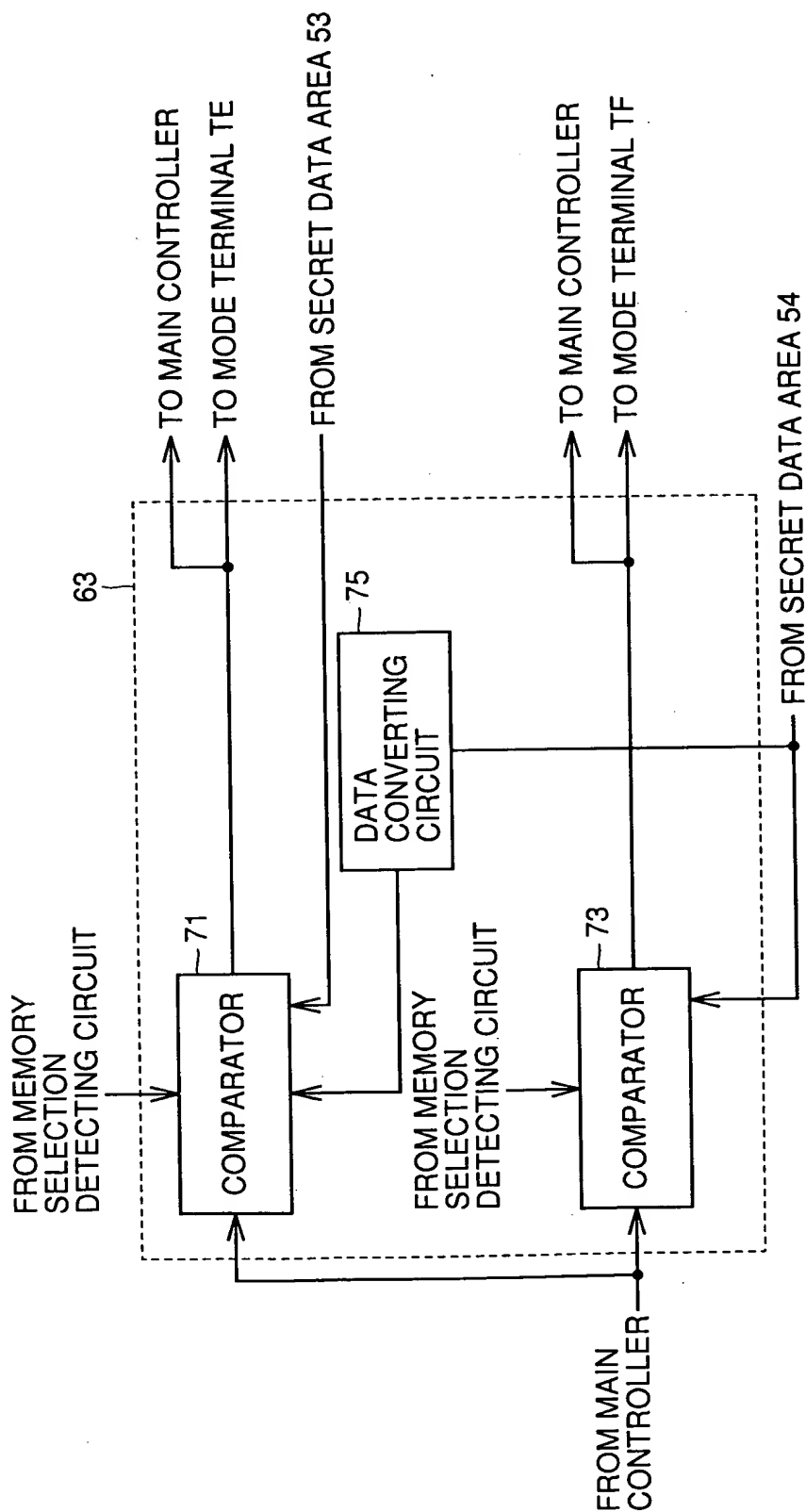


FIG. 8

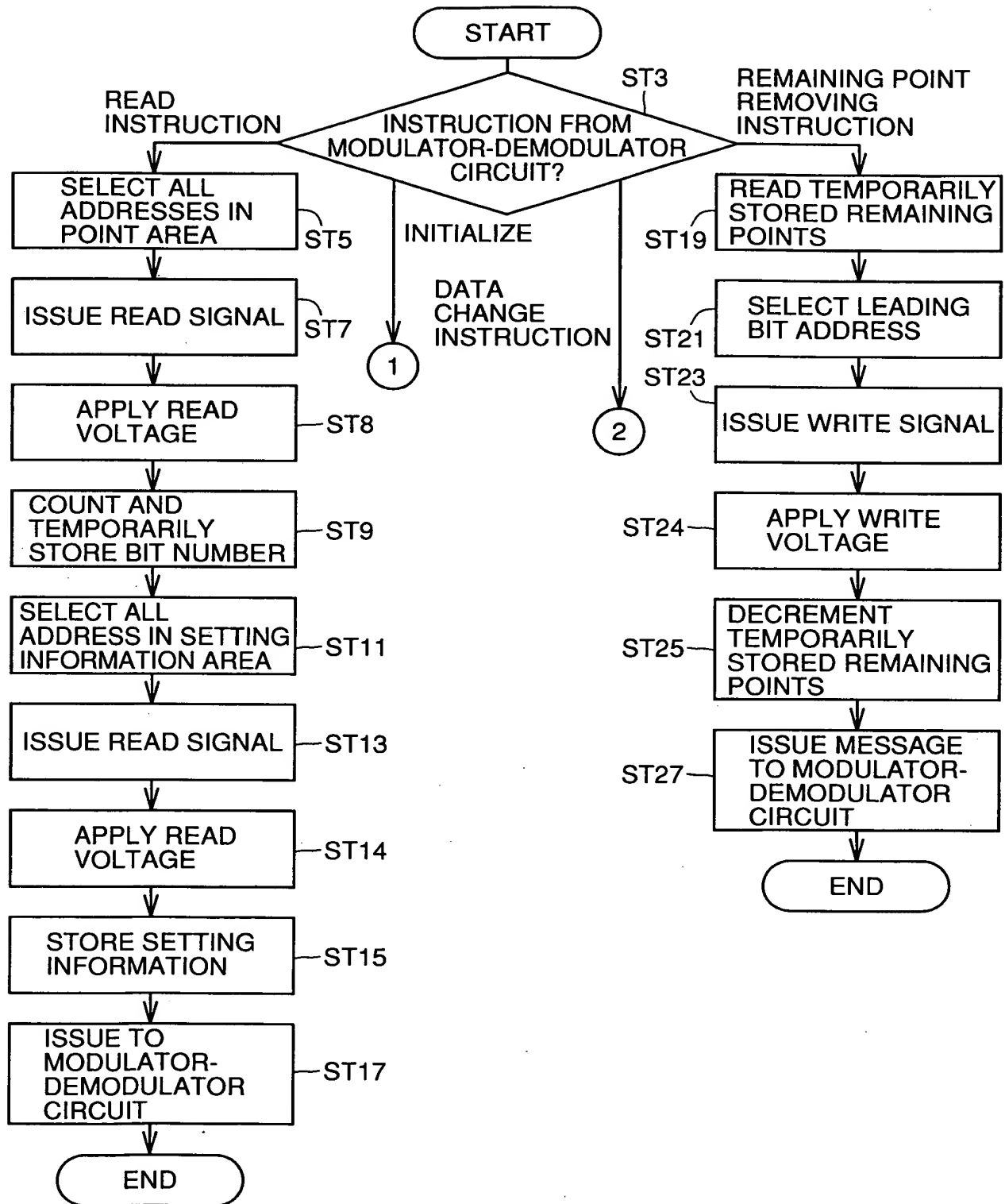
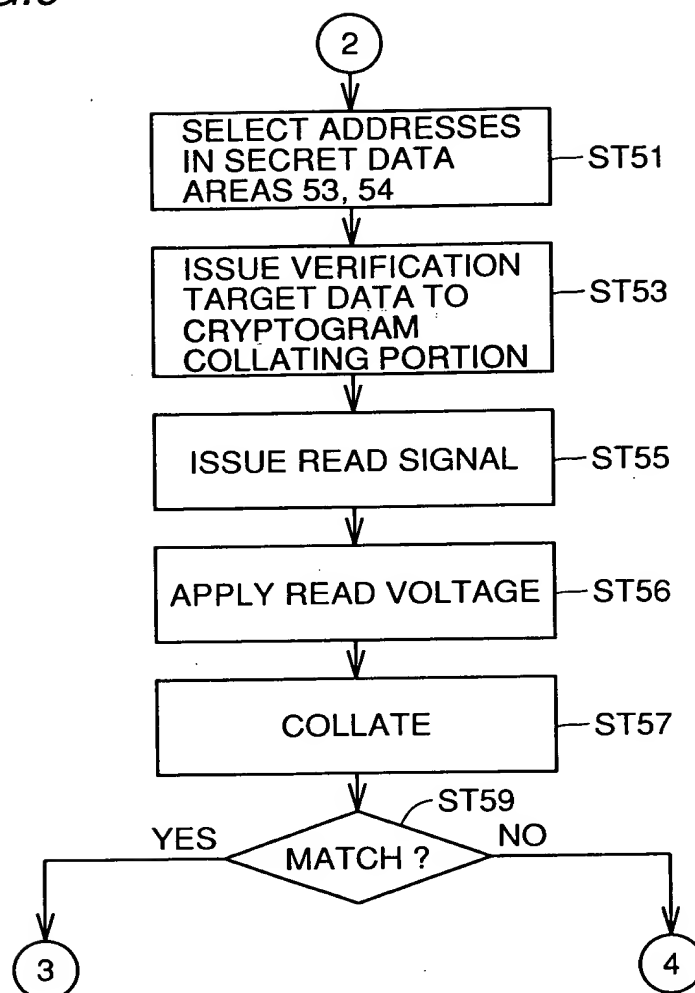
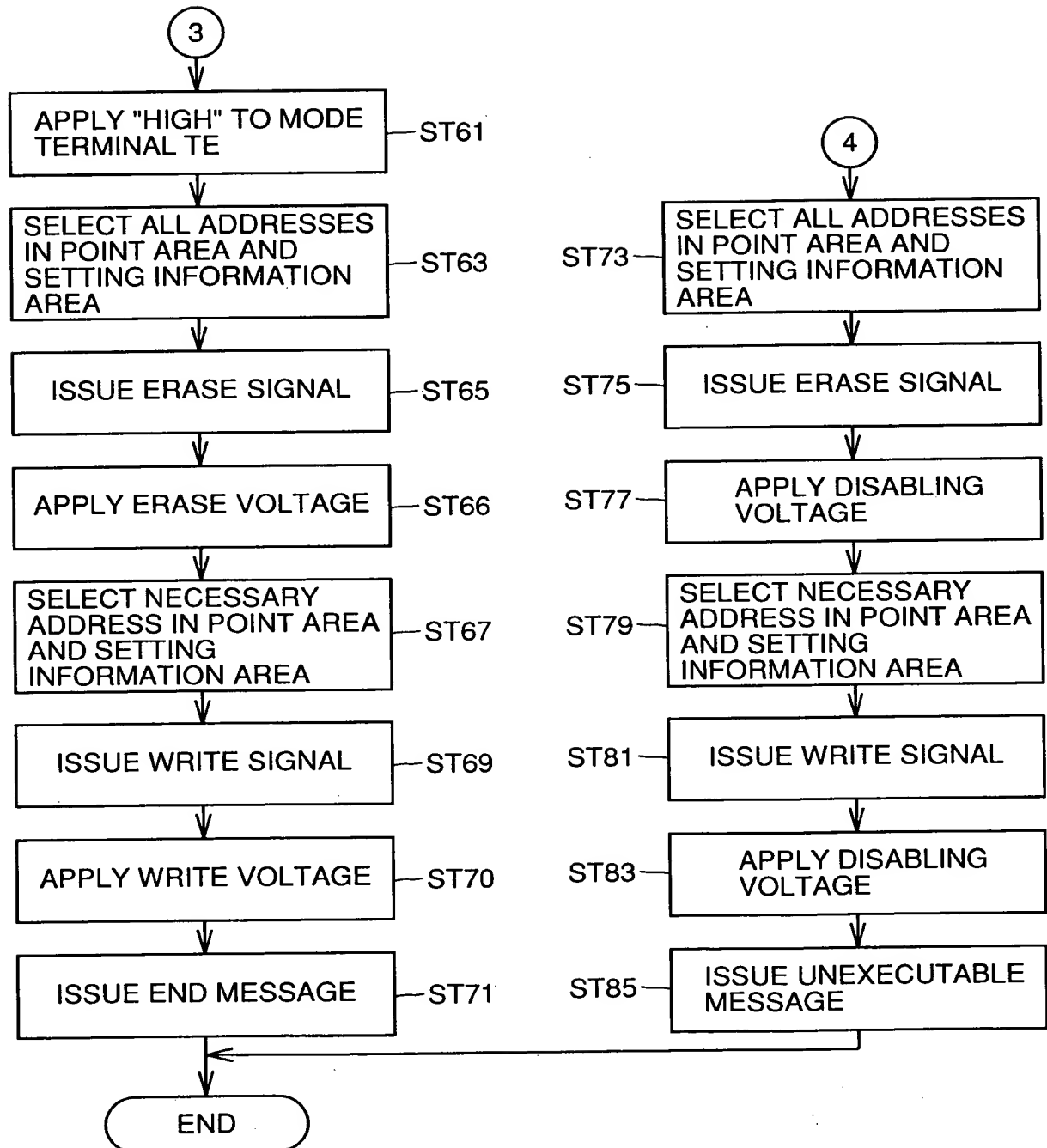


FIG. 9



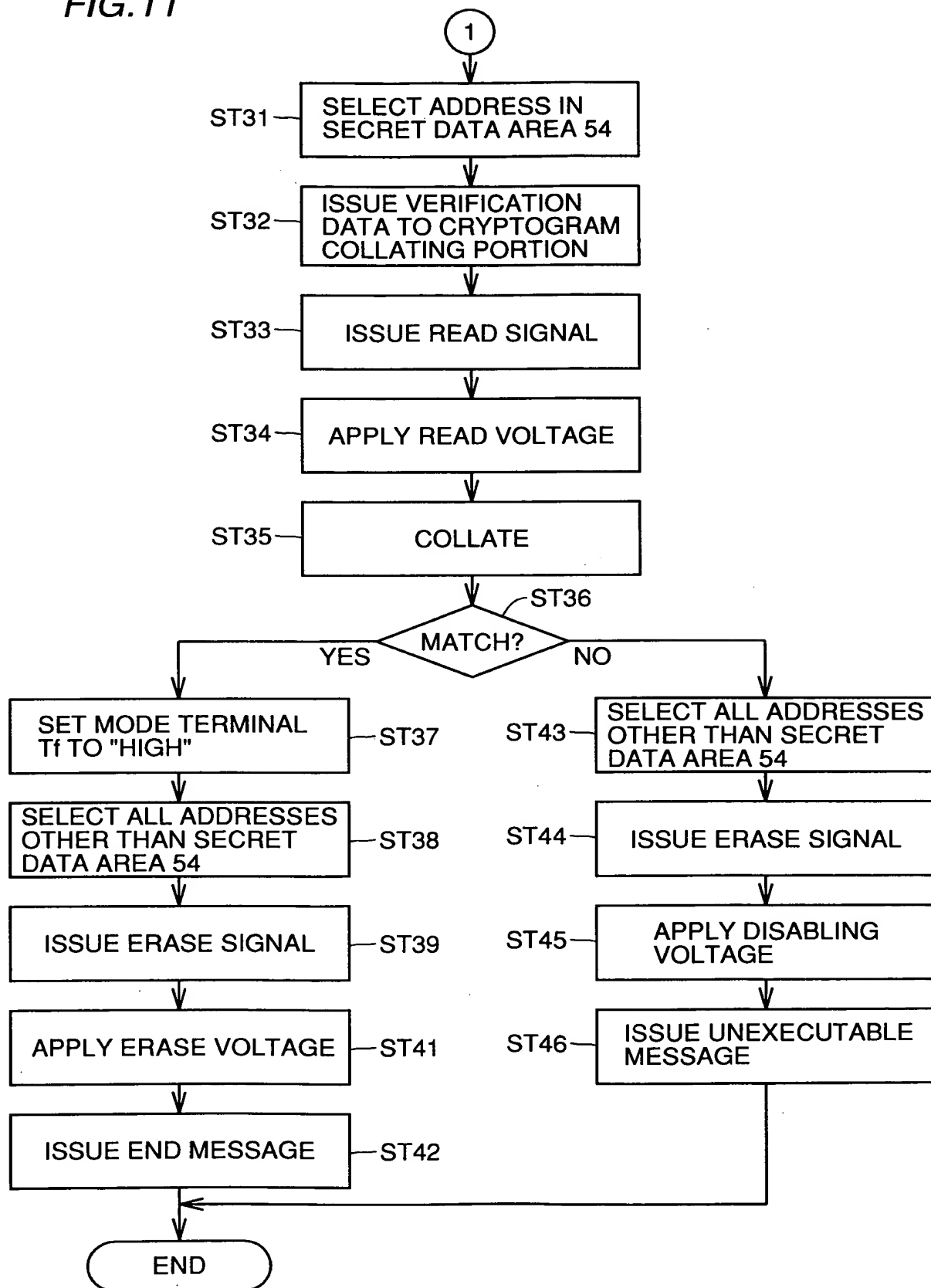
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FIG. 10



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FIG. 11



PRIOR ART

